

2020-2021 Master Cycle Program -

Tableau 1

BD - S1	BD	EMB	CYB	IOT	SMART	IA	ROB	E-HEA
Nomenclature	N°	N°	N°	N°	N°	N°	N°	N°
S7-UE1 Computer science	1E1	1E1	1E1	1E1	1E1	1E1	1E1	1E1
	1E2	1E2	1E2	1E2	1E2	1E2	1E2	1E2
	1E3	1E3	1E3	1E3	1E3	1E3	1E3	1E3
S7-UE3 - Système software & Hardware	1E4	1S4	1E4	1E4	1E4	1E4	1S4	1E4
	1E5	1S5	1E5	1E5	1E5	1A5	1S5	1E5
	1E6	1S6	1E6	1E6	1E6	1E6	1S6	1E6
S7-UE2- Plateforme développement	1E7	1S7	1E7	1E7	1E7	1E7	1S7	1E7
	1E8	1S8	1E8	1E8	1E8	1E8	1S8	1E8
	1E9	1E9	1C9	1E9	1E9	1E9	1E9	1E9
S7-UE4 Cloud deployment	1E10	1S10	1E10	1S10	1M10	1A10	1S10	1E10
	1E11	1S11	1C11	1S11	1S11	1E11	1S11	1E11
	1E12	1S12	1C12	1S12	1S12	1A12	1S12	1E12
S7-UE5 - Technical Project	1P1	1P1	1P1	1P1	1P1	1P1	1P1	1P1
S7-UE6 - SHES	1A	1A	1A	1A	1A	1A	1A	1A
S7-UE7 - Language	1H	1H	1H	1H	1H	1H	1H	1H
BD - S1	BD	EMB	CYB	IOT	SMART	IA	ROB	E-HEA
Nomenclature	N°	N°	N°	N°	N°	N°	N°	N°
S8-UE1 plateforme mobile	2E1	2S1	2E1	2E1	2S1	2E1	2S1	2E1
	2E2	2S2	2C2	2O2	2M2	2E2	2S2	2E2
		2S3		2S3	2M3		2S3	
S8-UE2 Security	2E4	2S4	2C4	2E4	2E4	2A4	2S4	2H4
	2E5	2S5	2S6	2S5	2S5	2E8	2S5	2H5
	2E6	2S6		2S6	2E6	2E6	2S6	2H6
S8-UE3 - Technical Project	2P1	2P1	2P1	2P1	2P1	2P1	2P1	2P1
S8-UE4 Environnement	2E7	2S7	2C7	2E7	2E7	2A7	2R7	2H7
	2E8	2S8	2C8	2O8	2O8	2A8	2R8	2H8
	2E9	2S9		2E9	2M9	2A9	2E9	2E9
S8-UE5 - Technical Project	2P2	2P2	2P2	2P2	2P2	2P2	2P2	2P2
S8-UE6 - SHES	1A	1A	1A	1A	1A	1A	1A	1A
S8 - UE7 - Language	1H	1H	1H	1H	1H	1H	1H	1H
BD - S1	BD	EMB	CYB	IOT	SMART	IA	ROB	E-HEA
Nomenclature	N°	N°	N°	N°	N°	N°	N°	N°
S9-UE1 Optimisation	3E1	3S1	3S1	3E1	3M1	3E1	University Program - Double Diploma	3H1
	3E2	3S2	3C2	3E2	3M2	3E2		3H2
	3E3	3S3		3E3	3M3	3E3		3H3
S9-UE2 -IA	3E5	3S4		3C4	3S4	3S4		3A4
		3S5	3S5		2M5	3A5		
	3E6	3S6	3C5	3S6	3S6	3E6		3E6
S9-UE3 - Technical Project	3P1	3P1	3P1	3P1	3P1	3P1		3P1
S9-UE4 Applied system	3E7	3S7	3C7	3O7	3E7	3A7		3H7
	3E8	3S8	3C8	3O8	3O8	3A8		3H8
	3E9	3S9		3O9	3O9	3A9	3H9	
S9-UE5 - Technical Project	3P2	3P2	3P13 P2	3P2	3P2	3P2	3P2	
S9 -UE6 - SHES	1A	1A	1A	1A	1A	1A	1A	
S9 -UE7 - Language	1H	1H	1H	1H	1H	1H	1H	

Cursus	Semester	Course	Course title	Titre FR	Keywords	Hourly	ECTS	Course type	Summary	Objectives	Prior knowledge	Assessment	Learning outcomes	Doc. language	Teach. language	Complementary information
Master 1 - Semestre 1 - FALL																
Master 1	S1 (Fall)	1E1	Basis databases BDD	Bases de données relationnelles	database, relational, management system	24	5/3	Lecture 4h Lab 20h	This lecture pursues the introduction to databases seen in third year. It begins with some recalls on the SQL language, presents new	Learning medium to advanced knowledge on relational databases	Linux shell, databases and SQL language basics, basics on relational model	MCQ each week, and written test at the end	use of database server, SQL operators, embedded programming on a database server	English	English	none
Master 1	S1 (Fall)	1E2	Linux shell	Systèmes d'exploitation (Shell)	kernel, shell, bash, scripting	24	5/3	Lab 24h	This lecture pursues the introduction to Linux shell seen in third year. Practical class is realized under Linux.	Learning medium to advanced knowledge on Linux shell	Fundamentals on Linux.	Written test.	Knowledge on Linux shell and bash scripting	French & English	English	none
Master 1	S1 (Fall)	1E6	AI and neural networks	Réseaux de neurones / Intelligence Artificielle	neural networks, AI, automated learning, logical reasoning	24	5/3	Lecture 10h Lab 14h	This lecture is an introduction to artificial intelligence, neural networks, automated learning, hybrid computation	Learning fundamental knowledge on neural networks and machine learning.	Fundamentals on programming languages and data structures.	Written test.	Knowledge on the most widely use theoretical and practical machine learning structures.	English	English	none
Master 1	S1 (Fall)	154	Physics of solids, semiconductors & devices	Physique du composant	Atoms, Solids, Band structure, Crystallography, Semiconductors, electrons & holes, space charge layer, PN junction	24	5/3	Lecture 12h Tutorial 12h	Starting from the electronic structure of atoms and molecules, we study the properties of solids from their electronic band structure.	- Basics in Physics of Solids - Understanding of the interest of semiconductors in the realization of electronic components	Basic quantum physics	Written test.	Understanding of the physics phenomena leading to currents in devices, the way to control them and basic calculations of currents in	English	English	[1] Physics of semiconductor devices, S.M.SZE, John Wiley & Sons (1981) D.L.K. SCHROEDER
Master 1	S1 (Fall)	155	Labview (Theoretical)	Labview Théorique	LabVIEW, National Instrument	24	5/3	Lecture and Lab	This lecture presents the basics of LabVIEW programming by introducing the way the language works and how it is used by	Learn how LabVIEW works, and get basic knowledge to prepare for CLAD certification	Basics in Informatics	MCQ	Labview programming theory	French & English	English	none
Master 1	S1 (Fall)	156	Conception of Digital circuits: From semiconductors to Integrated Circuits	Conception de circuits digitaux	Semiconductors, Integrated Resistance, MOS Capacitance, MOSFET, CMOS cells, FDSOI node	24	5/3	Lecture and tutorial	This course begins with a thorough review of actual digital circuits with a short history of circuit integration. Its goal is to develop a	- Knowledge of basic elements and components for the design of integrated circuits, focusing on the tradeoff between	Physics of semiconductors, Basics from electrostatics, Family Components, logic operation	Written test.	Know how to use proper modeling based on a degree of accuracy for a digital MOS circuit design application	English	English	Information for visiting students: Calculate the basic parameters for resistance, capacitance and channel
Master 1	S1 (Fall)	157	FPGA1	FPGA1	FPGA	24	5/3	Tutorial	- To understand the design process for implementing a digital design onto a FPGA - VHDL Programming - Step	Getting started with the FPGA and realization of different functional structures in VHDL language	VHDL	Written test	FPGA level 1 and VHDL coding	English	English	Download Vivado (required for using BASYS 3 board)
Master 1	S1 (Fall)	158	FPGA 2	FPGA 2	FPGA, VHDL, Cryptography	24	5/3	Tutorial	Introduction of cryptography solutions (AES, DES, ...) Cryptographic Applications using FPGA Technology FPGA implementation of AES	Using FPGA, on an cryptographic application	FPGA1	Written test	FPGA level 2 and cryptographic application (AES)	English	English	Bibliography FPGA Design - Best Practices Download Vivado (required for using BASYS 3 board)
Master 1	S1 (Fall)	1E9	Server-side programming with Python	Programmation dynamique coté serveur (PHP, Python)	python, django	24	5/3	Lecture 16h Lab 8h	This lecture introduces student to general programming with Python 3. Although most of them know the basics of Python, this	Learn beginner to medium knowledge with Python	Linux shell, databases, OOP, basic HTML, coding editor or IDE	Written test at the end	Python programming, good practices, Python landscape overview	English	English	Bibliography FPGA Design - Best Practices
Master 1	S1 (Fall)	1510	Hardware architecture and functional analysis	Analyse fonctionnelle hardware	Processor, ARM, architecture	24	1,5	Lecture	This lecture introduces the industrial design of processors by describing the ARM architecture	Learn how ARM processors work and how they are designed	Digital electronics, architecture	Written test	ARM architecture	french and english	English	none
Master 1	S1 (Fall)	1511	Hardware & software architecture for IoT	Architecture hardware orientée IoT	low level and high level architecture	24h	5/3	Lecture Tutorial	Sensors overview Main wired communication protocol Power consumption Processing	- Having a wide overview to hardware and software constraints in IoT application - Present a Benchmark of the main sensors / MCU / software	Basics in physics, electronics and computer science	Written test		English	English	
Master 1	S1 (Fall)	1512	RTOS	RTOS	Real Time Operating System, microcontroller, Embedded System	24	5/3	Lecture/Lab	This lecture is focused on Real Time Operating System for microcontroller. The students will implement FreeRTOS and exploit the	Learning fundamental of an RTOS	Programming C, MCU	Lab Work	RTOS experience, MCU experience	French & English	English	none
Master 1	S1 (Fall)	1513	Labview - Practice	Labview pratique	Labview Software	24	5/3	Tutorial	Labview Fundamental 1 - Exercices - Labview Navigation - Identification of problems and development of VIs	Getting started with Labview Software	Labview - Theory	Written test	Direct implementation of the theoretical Labview course	French & English	English	Information for visiting students: Download Labview software http://www.ni.com/white-paper/13413/en
Master 1	S1 (Fall)	1514	Labview FPGA	Labview - FPGA	Labview Software, FGPA	24	5/3	Tutorial	First LabVIEW FPGA Writing Program Implementing Counters in LabVIEW FPGA Using analog inputs and	To be able to realize an elementary functionality by using Labview FPGA...	Labview - Theory & Practice FPGA1 & FPGA2	Written test	Using the FPGA with an Labview Approach	English	English	Information for visiting students: To know Labview software To already have downloaded Labview software (classical) Download Labview software http://www.ni.com/white-paper/13413/en
Master 1	S1 (Fall)	1515	Labview - Certification	Labview Certification	Labview Software certification	24	5/3	Tutorial	Preparation of Labview Certification - CLAD	CLAD Certification	Labview - Theory Labview - Practice	Written test	Certification Labview	French & English	English	Bibliography
Master 1	S1 (Fall)	1A	Intermediate English	Anglais (niv. intermédiaire)	English	32	2		Acquisition and application of everyday language elements, using the book FCE Practice Test Plus 2 from Pearson. Training in reading various	At the end of this module, the student will have reached level B2 on the Common European Framework of Reference (CEFR) for	Being to sit the Cambridge First Certificate in English.	Written & Speaking	CECR B2 level	English	English	CECR B1 level
Master 1	S1 (Fall)	1H	Human, economic and social	Sciences humaines		40	3									
Master 1 - Semestre 2 - SPRING																
Master 1	S2 (Spring)	251	STM32 & Sensor Application	STM32 & Application Capteur	STM32, Programmation, Sensor	24	5/3	Lecture	This lecture consists in developing the experience of the students on microcontroller. It addresses the use of GPIO, Interruption	Learn how to program a microcontroller of 32 bits	Basic knowledge on microcontrollers	Lab work	Micronrollers programming	French & English	French	none
Master 1	S2 (Spring)	252	STM32 & Communication Application	STM32 & Application Communication	STM32, Communication	24	5/3	Tutorial	This lecture presents most used communications protocols as SPI, UART, I2C for STM32	Learn how to program a communication protocole on microcontroller of 32 bits	Basic knowledge on microcontrollers	Lab work	Micronrollers programming Communication protocols knowledge	French & English	French	none
Master 1	S1 (Fall)	153	Circuit Design	Conception analogique (filtrage, etc...)	Analog Design, Feedback effect, Filter	24	5/3	Lecture 8h Tutorial 16h	Getting start on Eagle software II - Reminder of important notions - Be able to realize a circuit	- Become familiar with the use of the softwares MultiSim (NI) Eagle - Learn the advantages of	Getting start Eagle software I, Concept of circuit design in analog and digital electronics (Electronics I, Logic II)	Written test.	Being able to realize an analog circuit responding to a given application	English	English	Bibliography https://www.autodesk.com/products/eagle/overview https://www.autodesk.com/products/eagle/free
Master 1	S2 (Spring)	254	Security & IC	Sécurisation des systèmes embarqués	Architecture & security introduction	24	5/3	Lecture	Security Cryptography, Smart Card architecture, Attack method but this course is nott A course on cryptography, hacking guide	Learn how to design & analyze a power electronics circuit to fulfill the recommendations	Fundamentals un electronics	Written test	Introduction of IC design & security	English	English / French	none
Master 1	S2 (Spring)	255	Architecture & Security	Archi & sécurisation	Architecture, Security	24	5/3	Lecture	Security Cryptography, Smart Card architecture, Attack method but this course is nott A course on cryptography, hacking guide	To place the Smart Card in its context To evidence its added-value in securing a system To identify its functional and security	STM32 Programmation	Lab work	Understand that security & design should be realized at the same time	English	French	none
Master 1	S2 (Spring)	256	Market & Hacking	Marché & Hacking	Hacker & Solution	24	5/3	Lecture/Tutorial	Introduction à la sécurité Hardware, Attaques Side Channel Attaques par injection de fautes, ...	Design et simulation de contremesures	MOS design, Cadence software	Lab work	How to protect a circuit from hardware and software attacks using hardware solutions	English	French	none
Master 1	S2 (Spring)	2P1	Technical project	Projet technique	Microcontroler, IC, Analog Design, System & Application	72	5	Lab 8h	This course consists in a technical project that students must carry on with the help of their tutor.	Problem Solving experience	none	Lab work.	Problem solving, electronics, informatics	French & English	French & English	none
Master 1	S2 (Spring)	2R7	Power electronics	Electronique de puissance théorique	Power Circuit	24	5/3	Lecture	This lecture introduces the basics of power electronics, by addressing step up & step down converters, buck-boos converters, fluback &	Learn how to design & analyze a power electronics circuit to fulfill the recommendations	Fundamentals un electronics	Written test	DC-DC converters designs, AC to DC / DC to AC converters design	French & English	French & English	none
Master 1	S2 (Spring)	2R8	Robot moving	Moteur commande optimisé	strategies de déplacement de robot basé sur l'image	24	5/3	Lab 24h	Application project with motors and image processing		Basic image processing and control motor	Lab work	Basic image processing and control motor	French	English	none
Master 1	S2 (Spring)	2E10	Advanced programming with C++	Programmation avancée C++	C++, programming, design patterns	24	5/3	Lecture 8h Lab 16h	This course presents to programmers the main differences between JAVA and C++ and approaches some advanced	Learning medium to advanced knowledge on C++ and design patterns.	C language, procedural programming, basis of object-oriented programming.	Written test.	Knowledge on how to design an application in C++ following the most well know design patterns.	French & English	French & English	none
Master 1	S2 (Spring)	2P2	Technical project	Projet technique	Application, Technical solution implementation	72	5	Lab 72h	Direct application of different lessons Applied practical work Project with an application selected by the students	Being able to apply the course with relative autonomy	STM32	Project validation		English	English	none
Master 1	S2 (Spring)	2A	Intermediate English	Anglais (niv. intermédiaire)	English	30	2		Acquisition and application of everyday language elements, using the book FCE Practice Test Plus 2 from Pearson. Training in reading various	At the end of this module, the student will have reached level B2 on the Common European Framework of Reference (CEFR) for	Being to sit the Cambridge First Certificate in English.	Written & Speaking	CECR B2 level	English	English	CECR B1 level
Master 1	S2 (Spring)	2H		SHES	Sciences humaines économiques et sociales	72	3									none
Master 2 - Semestre 1 - FALL																
University Program - Double Diploma																

